

SOURCE



SOURCE.SCH

CTRL



CTRL.SCH

RSENC



RSENC.SCH

ERROR



ERROR.SCH

RSDEC



RSDEC.SCH

SINK



SINK.SCH

CONFIG



CONFIG.SCH

CPU



CPU.SCH

SERIAL



SERIAL.SCH

CLOCK



CLOCK.SCH

SYNC



SYNC.SCH

PWR



PWR.SCH

Wideband Computers, Inc.
1350 Pear Avenue, Suite C
Mountain View, California 94043

Title

RS Eval Board - Top Level

Size Document Number

A

REV

0

Date: March 29, 1998 Sheet 1 of 9